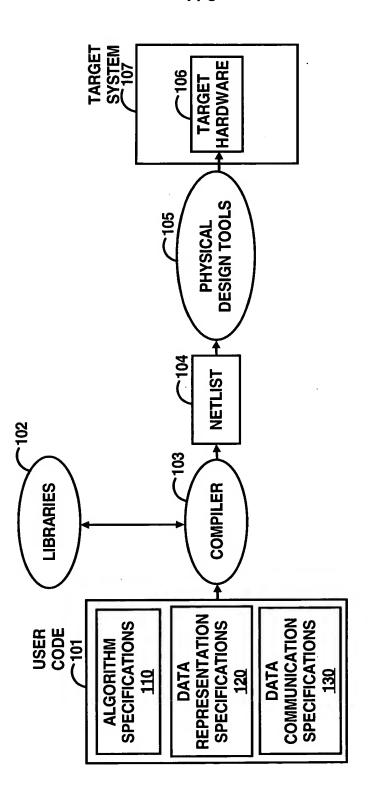
#### AN ALGORITHM-TO-HARDWARE SYSTEM AND METHOD FOR CREATING A DIGITAL CIRCUIT

Richard J. CARTER
HP Docket No.: 10008130-1

1/8



100

# AN ALGORITHM-TO-HARDWARE SYSTEM AND METHOD FOR CREATING A DIGITAL CIRCUIT Richard J. CARTER HP Docket No.: 10008130-1

2/8

200

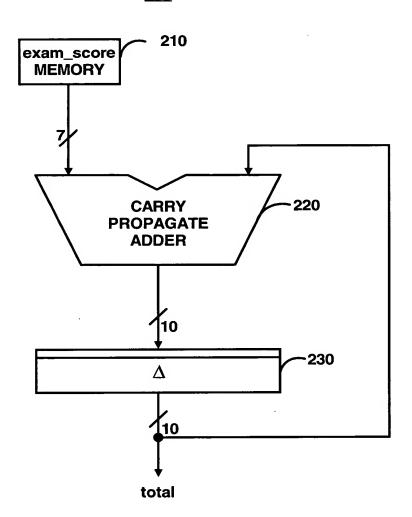


FIG. 2

## AN ALGORITHM-TO-HARDWARE SYSTEM AND METHOD FOR CREATING A DIGITAL CIRCUIT Richard J. CARTER HP Docket N .: 10008130-1

3/8

<u>300</u>

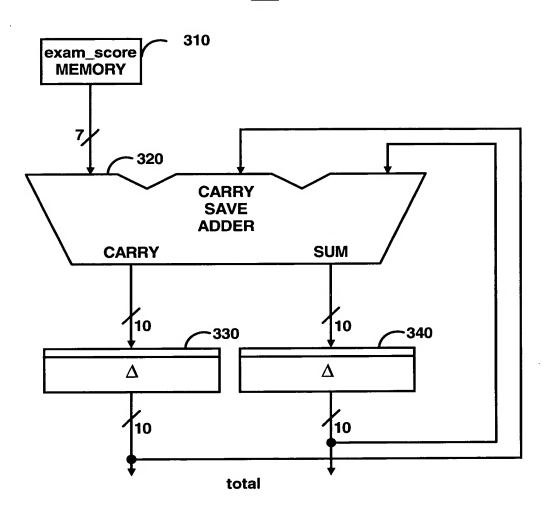


FIG. 3

### AN ALGORITHM-TO-HARDWARE SYSTEM AND METHOD FOR CREATING A DIGITAL CIRCUIT Richard J. CARTER

HP Docket No.: 10008130-1

4/8

exam\_score MEMORY 410

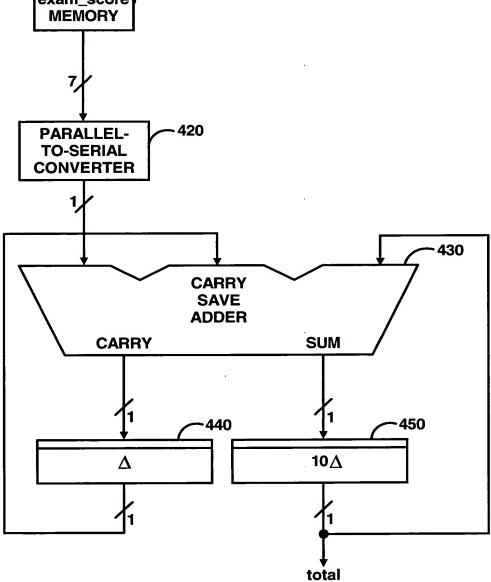


FIG. 4

# AN ALGORITHM-TO-HARDWARE SYSTEM AND METHOD FOR CREATING A DIGITAL CIRCUIT Richard J. CARTER HP Dock t N .: 10008130-1

5/8

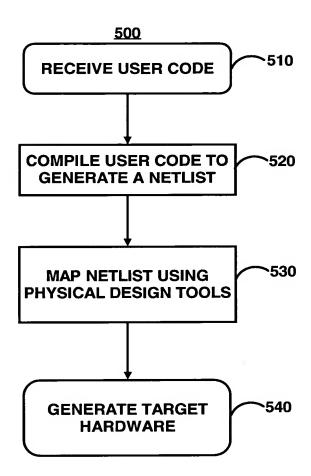


FIG. 5

## AN ALGORITHM-TO-HARDWARE SYSTEM AND METHOD FOR CREATING A DIGITAL CIRCUIT Richard J. CARTER

HP Docket No.: 10008130-1

6/8

<u>600</u>

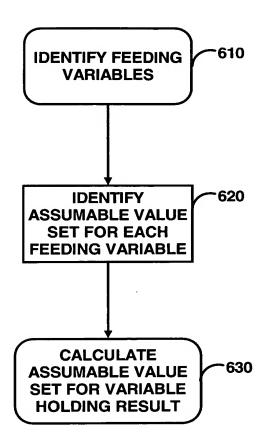


FIG. 6

## AN ALGORITHM-TO-HARDWARE SYSTEM AND METHOD FOR CREATING A DIGITAL CIRCUIT Richard J. CARTER HP Docket N .: 10008130-1

7/8

**700** 

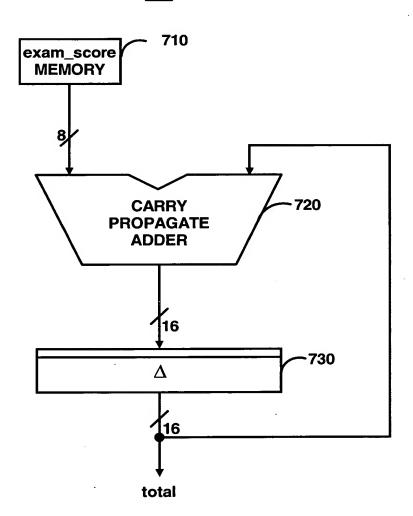


FIG. 7 Prior Art

#### AN ALGORITHM-TO-HARDWARE SYSTEM AND METHOD FOR CREATING A DIGITAL CIRCUIT Richard J. CARTER

HP Docket No.: 10008130-1

8/8

<u>800</u>

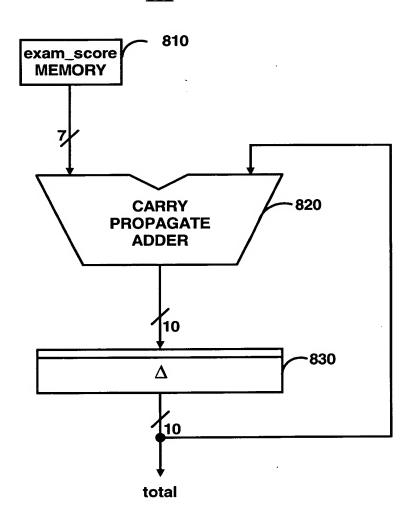


FIG. 8 Prior Art